

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE PATENT APPLICATION

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Nakamura et al.

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Title: DYNAMIC SEMICONDUCTOR MEMORY DEVICE AND BIT LINE

PRECHARGE METHOD THEREFOR.

Honorable Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

Applicants are hereby submitting certified copies of the foreign applications, Japanese Patent Application No. JP2003-128367 filed on May 6, 2003 as specified in 35 U.S.C. § 119(b).

Respectfully submitted,

Date: 7/14/04

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